



BOX AFTER FINAL

#24
Amndt E (108)
DSmalls Logon
Response Under 37 CFR §1.116
Expedited Procedure
Group Art Unit

Entered
6-11-03
Mollish

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

SHINOZAKI

Group Art Unit: 2816

Serial No.: 09/385,014

Examiner: Le, Dinh Thanh

Filed: August 27, 1999

Docket No.: 108075-09014

For: INPUT CIRCUIT HAVING CURRENT REGULATING TRANSISTOR

RESPONSE UNDER 37 CFR § 1.116

Commissioner for Patents
Washington, D.C. 20231

RECEIVED
FEB 12 2003
TECHNOLOGY CENTER
February 7, 2003
000

Sir:

*OK
FOR ENTRY
DR*
In response to the Office Action mailed October 7, 2002, with the date for response being extended from January 7, 2003 to February 7, 2003 by the attached Petition for extension of time, please consider the following amendment and remarks.

2/13
IN THE CLAIMS

Please amend claim 21 as follows:

*SB
F1
E1*
21. (Amended) A semiconductor integrated circuit for receiving a data signal in response to rising and falling edges of a data strobe signal, comprising:
a data strobe signal input circuit which receives the data strobe signal, wherein the data strobe signal input circuit includes: